



VIVEKANANDA GLOBAL UNIVERSITY, JAIPUR
Department of Computer Science & Engineering

(Established by Rajasthan State Legislature vide Act. No. 11/2012 and covered u/s 2(f) of UGC Act 1956)

VGU/2018-19/10506

29/11/2018

Minutes of the Meeting of the Board of Studies

Minutes of the meeting of the Board of Studies- Department of Computer Science Engineering, Faculty of Engineering & Technology, Held at 11:30 am on 28 November 2018 in Board Room of VIT- East.

Following members were Present:

1. Prof. Baldev Singh –Dean (Engineering)
2. Prof. Anil Choudhary, SKIT, External Member
3. Mr. Sitaram Gupta-Member, HOD CSE
4. Dr. Sanjay Kumar, Faculty Member, CSE
5. Mr. Dushyant Singh, Faculty Member CSE
6. Ms. Megha Sharma, Faculty Member, CSE
7. Shri R. K. Gupta-Secretary

Leave of absence was granted to:

8. Dr. M.C. Govil – MNIT, Jaipur, External Member
9. Dr. Sanjeev Agarwal – MNIT, Jaipur, External Member
10. Dr. Vijay Singh Rathore-JECRC, External Member

HOD CSE welcomed the members to the meeting & thereafter Member reviewed the scheme and syllabus of B.C.A, B.Tech., Diploma and M.Tech. program of Computer Science Engineering.

Following decisions were taken:

1. There are no changes in the Scheme & Syllabus of BCA, Diploma ,B.Tech.,M.Tech, Board of studies suggested to take the feedback from alumni/parents/industries and should be incorporate in the next meeting of BOS.
2. Paper setter panel for even semester 2018-19 is approved.

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29/11/18

Sitaram Gupta
Sitaram Gupta
HOD CSE

Copy to: Hon'ble Chairman/ Vice-Chairperson/Director general/Registrar VGU/ President/Pro. President VGU/ All Deans VGU /All HODs VGU/Office file/Exam Cell

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