



**VIVEKANANDA GLOBAL UNIVERSITY, JAIPUR**  
**Department of Electrical Engineering**

(Established by Rajasthan State Legislature vide Act. No. 11/2012 and covered u/s 2(f) of UGC Act 1956)

VGU/EE/18-19/ 1418

Date: 03, Dec, 2018

## **Minutes of the Meeting of the Board of Studies**

**Minutes of the Meeting of the Board of Studies-** Department of Electrical Engineering, Faculty of Engineering & Technology, Held at 10:30 am on 30, Nov, 2018 in Board Room of VIT- East.

Following members were present:

1. Prof. Baldev Singh – Dean (Engineering)
2. Dr. Ramesh Pachar- SKIT, External Member,
3. Shri B. N. Saini, Ex-Director, RVPNL, External Member,
4. Mr. Parmeshwar Kumawat-Member, HOD EE
5. Mr. Atul Dadhich, Faculty Member EE
6. Mr. Mohit Jain, Faculty member, EE
7. Shri R. K. Gupta-Secretary

**Leave of absence was granted to:**

8. Dr. K R Niazi – MNIT, Jaipur, External Members
9. Dr. Rajesh Kumar – MNIT, Jaipur, External members

HOD EE welcomed the members to the meeting & thereafter Member reviewed the scheme and syllabus of B.Tech., Diploma and M.Tech. program of Electrical Engineering.

**Following decision were taken.**

1. Reviewed and discussed scheme and syllabus of M.Tech. B Tech. and Diploma program and recommended that no changes are required in current scheme and syllabus .



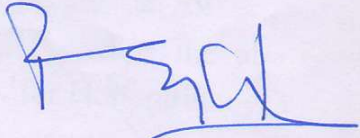
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2. BOS suggested to take feedback from alumni/parents/industries to review scheme and syllabus should be incorporate in the next meeting of BOS.
3. After Reviewing the paper setter panel of M.Tech., B Tech. and Diploma program for even semester 2018-19 approved without changes.

**Thereafter the meeting ended with a Vote of Thanks to the Chair.**

*Parup*  
*3/12/18*  
*J*

  
**R. K. Gupta**  
Member secretary, BOS